

References Category:Rigid body dynamics1. Field of the Invention The present invention relates to a nonvolatile semiconductor memory device in which an erase operation is controlled, and more particularly, to a nonvolatile semiconductor memory device which stores information in a charge accumulation region by injecting electrons and suppresses an erasing time from being prolonged. 2. Description of the Related Art Nonvolatile semiconductor memory devices including flash memory are being used in mobile devices such as mobile phones because of the advantages of being able to make data holding permanent and being able to store various kinds of information including music, text and the like. Flash memory is capable of storing a large amount of information within a small area. In a flash memory, an erasing operation for the purpose of recording information in a charge accumulation region and a write/erase operation for the purpose of reading information out of the charge accumulation region are both performed by an F-N (Fowler-Nordheim) tunneling current. In an erasing operation, as shown in FIG. 6, a high voltage is applied to a substrate of a flash memory, and electrons in the charge accumulation region are extracted to the substrate side to cause a positive change in the potential. In a write operation, as shown in FIG. 7, a low voltage is applied to the substrate, and electrons are injected to the charge accumulation region to cause a negative change in the potential. In this case, the charge accumulation region (for example, a channel) is formed in a surface semiconductor layer. An example of the erase operation of a nonvolatile memory will be described. First, a high voltage is applied to a control gate electrode, and a low voltage is applied to a semiconductor substrate. Next, electrons are extracted to the semiconductor substrate side, and a positive change in the potential of a channel is caused to cause a negative change in the charge accumulation region. In this case, as shown in FIG. 7, a negative change in the potential is caused by extracting electrons from the charge accumulation region to the semiconductor substrate side in a negative direction, whereby information is held in a negative form. In a write/erase operation, as shown in FIG. 6, the potential of a control gate electrode is changed from a high potential to a low potential, whereby a write operation is performed. The potential of the semiconductor substrate side is fixed at a negative potential by the high potential applied to the control gate electrode.

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